AMENDMENTS TO THE SPECIFICATION

Please replace the paragraph beginning at page 1, line 24, with the following rewritten paragraph:

In recent years, digital still cameras as an electronic imaging apparatuses (i.e., electronic cameras) have been intensively developed which are adapted to capture the image of a subject on the built-in image pick-up device (hereinafter referred to as the imaging device), such as a CCD, and produce a corresponding still or video image signal to reproduce the image. In [[the]] recent digital [[camera]] cameras, the number of pixels are so increased as to have an improved image quality, but it is still required that shots [[are]] be continuously ~~photographed~~ taken at [[a]] short time ~~period~~ intervals. Accordingly, it is necessary to improve a circuit system of processing an image so as to satisfy the above requirement. That is, there is required ~~to propose~~ an improved method of reading an image formed on an imaging device having an increased number of pixels at a high speed-reading rate and a high frame transfer rate. To achieve the requirement, in such imaging apparatuses, for example, in still-image or moving-image apparatuses, it is said that, though image resolution is reduced, the sensitivity of the imaging device can be increased by adding together signal charges from adjacent pixels of same color, for example, a total of four pixels: two pixels of same color adjacent to each other in the vertical direction and two pixels of same color adjacent to each other in the horizontal direction.

Please replace the paragraph beginning at page 6, line 8, with the following rewritten paragraph:

It is an object of the present invention to provide an imaging apparatus which permits pixel signals to be added together without need of complex operation processing to increase the sensitivity and allow the frame rate to be increased.

Please replace the paragraph beginning at page 6, line 26, with the following rewritten paragraph:

According to an aspect of the present invention there is provided an apparatus comprising: a color imaging device having [[an]] interline transfer charge transfer paths adapted

for interlaced readout and a Bayer-arrangement color filter; driving means for driving the imaging device which, in addition to normal signal read operation, allows addition readout for reading signals while adding together signal charges from a plurality of pixels arranged in the vertical direction in the charge transfer paths; addition operation means for performing addition operation of a plurality of pixels arranged in the horizontal direction on pixel signals read from the imaging device; and mode setting means for setting selectively one of a first shooting mode corresponding to the normal readout operation of the imaging device and a second shooting mode,

when the second shooting mode is set by the mode setting means, the driving means performing the addition readout on the plurality of pixels in the vertical direction and the addition operation means performing addition processing on alternate pixels in the horizontal direction.